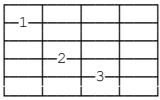
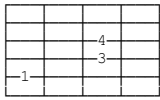
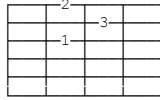
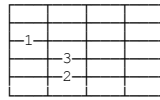
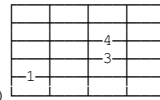
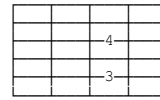
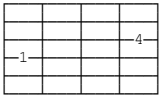
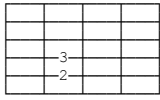
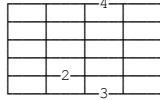
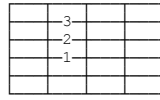
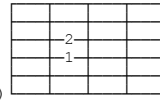
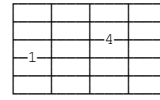
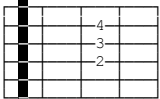
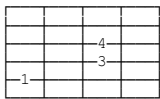


C  (x)	C#m7/E  IV	D  (x) x	E 	E5  (x) VII	E5/F#  x x VII
E5/G#  x (x) VI	Em 	G 	A  (x)	Asus2 A2  (x)	VII  x x
H  (x) II	Hsus4/E H4/E  II				