

D

	2			
	1	3		
(x)				
x				

Dsus2
D2

			3	
	1			
(x)				
x				

Dsus4
D4

			4	
			3	
(x)		1		
x				

Em

		3		
		2		

F#m

			4	
			3	

II

G

			4	
		2		
			3	

A

		3		
		2		
		1		
(x)				

Asus2
A2

		2		
		1		
(x)				

Hm

		2		4
				3
(x)				

II